CP40

- 16 KByte Application Program Memory (4.7 K Instructions)
- Processing Time 4 msec/K Instructions
- 7168 Registers
  800 Flags
- MOTOROLA 6303 Microprocessor
- Hardware Watchdog
- Status Display, Reset Button, Status LED
- Date/Time Function (Software Clock)
- Software Compatible to all Type A CPUs

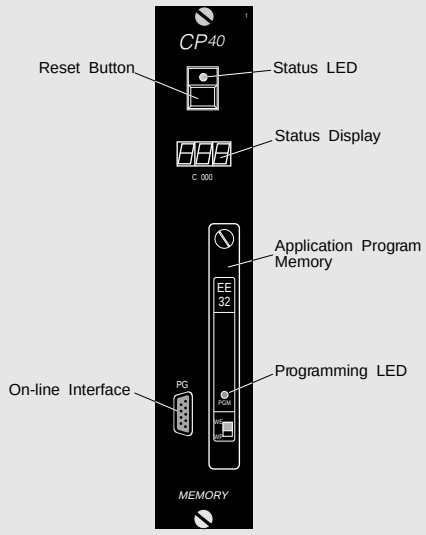
SLOTS

The CP40 CPU can be used in racks MULTI and MIDI[1].

ORDER DATA

ECCP40-01	MULTICONTROL CPU Type A, 16 KByte Application Program Memory for 4.7 K Instructions, Processing Time 4 msec/K Instructions, 7168 Registers, 800 1 Flags, No Application Program Memory Module

OPERATIONAL ELEMENTS



TECHNICAL DATA	CP40
Rack MULTI, MIDI[1]	
Processor	MOTOROLA 6303
Processing Time	4 msec/K Instructions
Registers	7168
Remnant	7148
Non-Remnant	20
Flags	800
Remnant	300
Non-Remnant	500
Application Program Memory (Not incl.)	EE32
Reset Button	YES
Status Display	YES
Time/Date	Software Clock, Volatile
Number of I/O	
Digital	1536
Analog	256
Serial Interfaces	
On-line Interface	TTY (62.5 kBaud)
Application Interface	-
Hardware Timers	512
Software Timers	64
Timing Pulse/Timing Cycle	10 msec, 100 msec, 1 sec, 10 sec
Power Consumption	
At +8 V	5 W
At +15 V	-
At -30 V	-
Documentation	Hardware Manual MULTICONTROL
German	MAHWMULTI-0
English	MAHWMULTI-E
French	MAHWMULTI-F
Italian	MAHWMULTI-I
Spanish	MAHWMULTI-S

PROGRAMMING

Programming the CP40 is done with the B&R PROgramming SYStem. Powerful standard function blocks are used for creating the program. The B&R PROgramming SYStem and standard software packages are described in section A7 "PLC Programming".

The application program memory module is not included with the CP40 CPU and must be ordered separately. A description of the application program memory module for the CP40 CPU can be found in section "Application Program Memory Modules".

[1] If the CP40 is to be operated in a MIDI rack, slot 0 cannot be used for an application module.