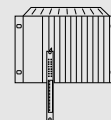


# CPUS, CP60 - MULTICONTROL CPU TYPE B

PLC SYSTEMS  
MULTICONTROL COMPONENTS

# A6



## CP60

- 42 KByte Application Program Memory (Max. 42 K Instructions)
- Optional Processing Time<sup>1)</sup> (1.7 or 2.5 msec/K Instructions)
- 7168 Registers  
800 Flags
- MOTOROLA 6809 Microprocessor
- Hardware Watchdog
- Status Display, Reset Button, Status LED
- Date/Time Function (Real-Time Clock)
- Software is Compatible to all Type B CPUs and Parallel Processors

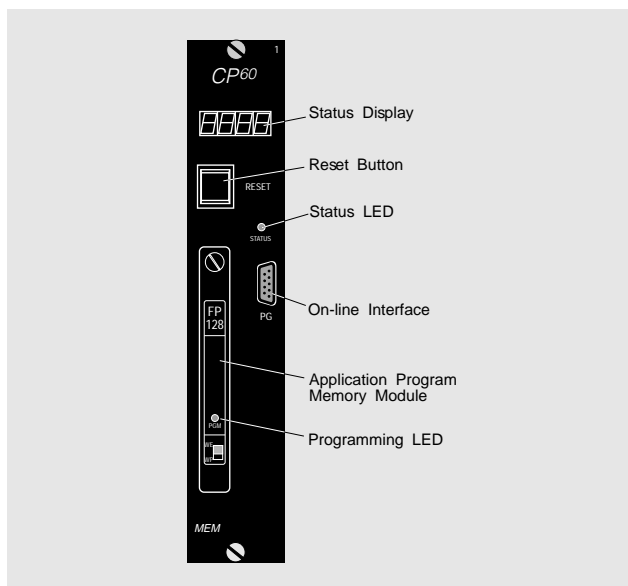
### SLOTS

The CP60 CPU can be used with racks MULTI and MIDI<sup>2)</sup>.

### ORDER DATA

**ECCP60-01** MULTICONTROL CPU Type B, 42 KByte Application Program Memory for Max. 42 K Instructions, Processing Time<sup>1)</sup> Optional (1.7 msec or 2.5 msec/K Instructions), 7168 Registers, 800 Flags, Real-Time Clock, No Application Program Memory Module

### OPERATIONAL ELEMENTS



### TECHNICAL DATA

### CP60

Rack MULTI, MIDI <sup>2)</sup>	
Processor	MOTOROLA 6809
Processing Time <sup>1)</sup> Optional	
2 MHz	2.5 msec/K Instructions
3 MHz	1.7 msec/K Instructions
Registers	7168
Remnant	7148
Non-Remnant	20
Flags	800
Remnant	300
Non-Remnant	500
Application Program Memory	42 KByte RAM (for Max. 42 K Instructions) On Module, PROM Module Not Included
Reset Button	YES
Status Display	YES
Time/Date	Real-Time Clock, Non-Volatile
Number of I/O	
Digital	1536
Analog	256
Serial Interfaces	
On-line Interfaces	TTY (62.5 kBaud)
Application Interface	-
Hardware Timers	512
Software Timers	64
Timing Pulse/Timing Cycle	10 msec, 100 msec, 1 sec, 10 sec
Power Consumption	
At +8 V	3.9 W
At +15 V	2.0 W
At -30 V	-
Documentation	Hardware Manual MULTICONTROL
German	MAHWMULTI-0
English	MAHWMULTI-E
French	MAHWMULTI-F
Italian	MAHWMULTI-I
Spanish	MAHWMULTI-S

### PROGRAMMING

Programming the CP60 is done with the B&R PROgramming SYStem. Powerful standard function blocks are used for creating the program. The B&R PROgramming SYStem and standard software packages are described in section A7 "PLC Programming".

The PROM application program memory module (EPROM, EEPROM or Flash-PROM) is not included with the CP60 CPU and must be ordered separately. A description of the application program memory module for the CP60 CPU can be found in section "Application Program Memory Modules".

<sup>1)</sup> With revision 59.xx of type B CPUs, the processing time can be switched from 2.5 msec/K to 1.7 msec/K instructions.

<sup>2)</sup> If the CP60 is run in a system with the MIDI rack, slot 0 cannot be used for application modules.