CP70

- 42 KByte Application Program Memory (max. 42 K Instructions)

- Processing Time[1] Optional (1.7 or 2.5 msec/K Instructions)

- 7168 Registers 800 Flags

- MOTOROLA 6809 Microprocessor

- Hardware Watchdog

- Status Display, Reset Button, Status LED

- RS232/RS485/TTY Application Interface

- Date/Time Function (Real-Time Clock)

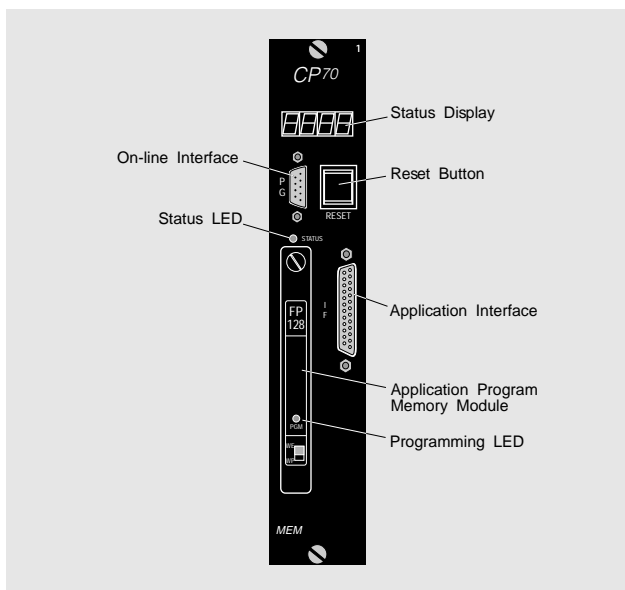- Software is Compatible to all Type B CPUs and Parallel Processors

SLOTS

The CP70 CPU can be operated in the MULTI and MIDI[2] racks.

ORDER DATA

ECCP70-01 MULTICONTROL CPU Type B, 42 KByte Application Program Memory for Max. 42 K Instructions, Processing Time[1] Optional (1.7 msec or 2.5 msec/K Instructions), 7168 Registers, 800 Flags, Real-Time Clock, No Application Program Memory Module

OPERATING ELEMENTS

TECHNICAL DATA	CP70
Rack MULTI, MIDI[2]	
Processor	MOTOROLA 6809
Processing Time[1] Optional	
2 MHz	2.5 msec/K Instructions
3 MHz	1.7 msec/K Instructions
Registers	7168
Remnant	7148
Non-Remnant	20
Flags	800
Remnant	300
Non-Remnant	500
Application Program Memory	42 KByte RAM (for Max. 42 K Instructions) On Module, PROM Module Not Included
Reset Button	YES
Status Display	YES
Time/Date	Real-Time Clock, Nonvolatile
Number of I/O	
Digital	1536
Analog	256
Serial Interfaces	
On-line Interface	TTY (62.5 kBaud)
Application Interface	RS232/RS485/TTY
Hardware Timers	512
Software Timers	64
Timing Pulse/Timing Cycle	10 msec, 100 msec, 1 sec, 10 sec
Power Consumption	
At +8 V	8,9 W
At +15 V	0.9 W
At -30 V	0.8 W
Documentation	MULTICONTROL CPU CP70 User's Manual
German	MACP70KB-0
English	MACP70KB-E

PROGRAMMING

Programming the CP70 is done with the B&R PROgramming SYStem. Powerful standard function blocks are used for creating the programs. The B&R PROgramming SYStem and standard software packages are described in section A7 "PLC Programming".

The PROM application program memory module (EPROM, EEPROM or Flash-PROM) is not included with the CP70 CPU and must be ordered separately. A description of the application program memory module for the CP70 CPU can be found in section !Application Program Memory Modules".

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[1] With revision 59.xx of type B CPUs, the processing time can be switched from 2.5 msec/K to 1.7 msec/K instructions.

[2] If the CP70 is run in a system with the MIDI rack, slot 0 cannot be used for application modules.