PP60 MEM

- Parallel Processor Type B with 128 KByte Data Memory
- 42 KByte Application Program Memory for 42 K Instructions
- Processing Time approx. 2.5 msec per K Instructions
- 11264 Registers
- Serial RS485/RS232/TTY Interface
- Software Clock

SLOTS

The PP60 MEM parallel processor can be operated in the MULTICONTROL and MIDICONTROL module racks in the following slots.

Module Rack	Slot	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
MULTI Main Rack		●	●	●	●	●	●	●	●	●	●	●	●	●	●	●	●
MULTI Expansion Rack		○	○	○	○	○	○	○	○	○	○	○	○	○	○	○	○
MIDI		○	●	●	●	●	●	●	●								
M264		○	●	●	●	●	○	○	○	○	○	○					

● the module can be operated in this slot
○ the module **cannot** be operated in this slot

ORDER DATA

ECPP60MEM-01	Parallel Processor Type B, 6809 processor, 42 KByte application program memory for 42 K instructions, processing time approx. 2.5 msec per K instructions, 11264 registers, serial RS485/RS232/TTY user interface, without application program memory module, 128 KByte data memory (RAM)

ADDITION DATA MEMORY

The parallel PP60 MEM processor provides 128 KByte data memory (static RAM) in addition to the functions of a PP60 parallel processor. This memory area is buffered by the battery in the power supply module and by the battery in the parallel processor. It is also nonvolatile if the module is removed from the PLC.

Addressing

The parallel processor accesses the 128 KByte data memory via P addresses. Then desired memory location ($0000 to $FFFF) is addressed with a 16 bit address register. The selected memory location is read or written to with an access register. Access can also be performed with an auto-increment access register. That means the address register is automatically incremented after the access. Auto-increment access registers are very useful for copy loops.

TECHNICAL DATA	PP60 MEM
Module Rack	MULTI, MIDI
Processor	6809
Processor Time	2.5 msec/K instructions
Registers	11264
Remanent	11244
Non-remanent	20
Application Program Memory	42 KByte RAM (internal), PROM Module (EPROM, EEPROM, FlashPROM) not incl. for 42 K instructions
Time /Date	Software clock, volatile
Serial Interfaces	
Online Interface	TTY (62.5 kBaud)
User Interface	RS485/RS232/TTY (19.2 kBaud)
Power Consumption	
at +8 V	7 W
at +15 V	1.5 W
at -30 V	0.5 W
Documentation	MULTICONTROL Hardware Manual
German	MAHWMULTI-0
English	MAHWMULTI-E
French	MAHWMULTI-F
Italian	MAHWMULTI-I
Spanish	MAHWMULTI-S

PROGRAMMING

Programming the PP60 MEM parallel processor is carried out with the B&R PROgramming SYStem. Efficient standard function blocks are available. The B&R PROgramming SYStem and standard software package are described in Section A7 "PLC Programming".

The application program memory module is not included with the delivery of the PP60 MEM parallel processor, it must be ordered separately. A description of the application program memory module can be found in Section "Application Program Memory Module".

STANDARD FUNCTION BLOCKS

Data can be read from data memory or written to data memory with the standard function blocks GETM and PUTM. The function block GETM can copy up to 1024 Bytes from data memory into the register area of the PP60 MEM. The function block PUTM can copy up to 1024 Bytes from the register area of the PP60 MEM into the data memory.