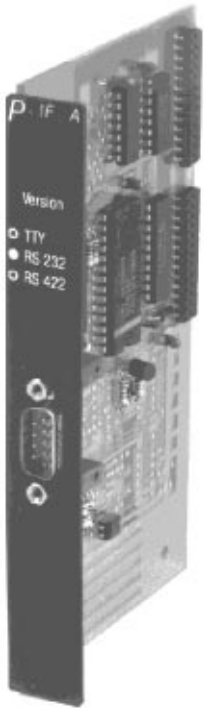


A4

INTERFACE MODULES, PIFA - SERIAL RS232 INTERFACE

PLC SYSTEMS
MINICONTROL COMPONENTS



PIFA

- 1 Serial Standard RS232 Interface
- Baudrate - Software Definable up to 19200 Baud

TECHNICAL DATA

PIFA

Interface	RS232
Galvanic Isolation	
Sender	NO
Receiver	NO
Connection	9 pin D-type (Male)
Max. Distance	10 m
Handshake Lines	DSR, DTR, RTS
Baudrates	50 to 19200 Baud, software setting
Data Formats	5 to 8 Data Bits, Parity Yes/No/Even/Odd, 1/1.5/2 Stop Bits, Selected with software
Documentation	Hardware Manual MINICONTROL
German	MAHWMINI-0
English	MAHWMINI-E
French	MAHWMINI-F

PIN ASSIGNMENTS

Pin

RS232

	1	GND
	2	RXD
	3	
	4	DSR
	5	DCD
	6	+5 VDC / 200 mA (Rev. 02.00 and higher)
	7	TXD
	8	RTS
	9	DTR

SLOTS

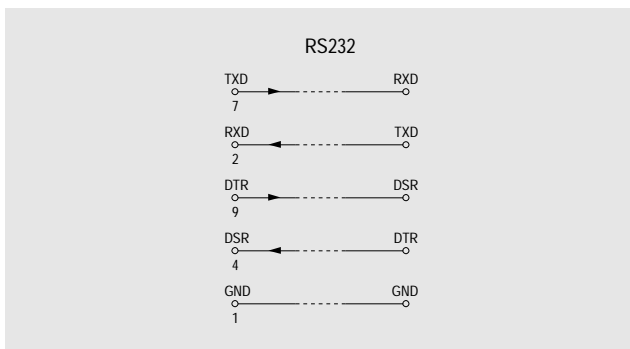
0 1 2 3 4 5

PIFA	Base Unit C (CP32)	● ●
------	--------------------	-----

ORDER DATA

MCPIFA-2	Interface Module, 1 Serial RS232 Interface, 9 pin D-type Male
----------	---

CONNECTIONS



STANDARD SOFTWARE

The following standard function blocks are available for the PIFA interface module:

FBK	Function
NDMA	Driver function block for connecting to a B&R MININET network (see section C5 - "B&R MININET")
OPIB	Driver function block for controlling the BRRT360 operator panel BRRT360 (see section B2 "Visualization with Operator Panels")
MCDA	Driver function block for controlling BRMEC mass memory device (see section A6 "MULTICONTROL Components")

The function block OPIB (Operator Panel Driver) and MCDA (BRMEC Driver) are components of the standard software package SWSPSTD02-0 (see section A7 "PLC Programming").

The NDMA function block (B&R MININET driver) is a component of the standard software package SWSPSCOM01-0 (see section A7 "PLC Programming").

A detailed description of the standard function blocks for the PIFA interface module shown above can be found in the "Standard Software User's Manual":

FBK	Volume	Chapter
NDMA	2	Chap. 6 "B&R MININET"
OPIB	1	Chap. 4 "Operator Panels"
MCDA	2	Chap. 11 "Device Driver"